



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/053,733	01/18/2002	Jonathan Christopher Hardwick	MS164198.1/40062.162US01	4234
22801	7590	10/05/2005	EXAMINER	
LEE & HAYES PLLC 421 W RIVERSIDE AVENUE SUITE 500 SPOKANE, WA 99201			ALHIJA, SAIF A	
			ART UNIT	PAPER NUMBER
			2128	
DATE MAILED: 10/05/2005				

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No. 10/053,733	Applicant(s) HARDWICK ET AL.	
	Examiner Saif A. Alhija	Art Unit 2128	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 18 January 2002.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-39 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-39 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____ |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| Paper No(s)/Mail Date <u>6/18, 21/04, 9/22/03</u> . | 6) <input type="checkbox"/> Other: _____ |

12

DETAILED ACTION

1. Claims 1-39 have been presented for examination based on the application filed on 18 January 2002.

Information Disclosure Statement

2. The information disclosure statements (IDS) submitted on 21 June 2004, 18 June 2004, and 22 September 2003 are in compliance with the provisions of 37 CFR 1.97. Accordingly, the Examiner has considered the IDS' as to the merits.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

3. **Claims 1-39 are rejected under 35 U.S.C. 102(b) as being clearly anticipated by E. Papaefstathiou "Design of a Performance Technology Infrastructure to Support the Construction of Responsive Software", hereafter referred to as Papaefstathiou.**

Regarding Claims 1, 18, and 30:

Papaefstathiou discloses a computer program product encoding a computer program for executing on a computer system a computer process for simulating performance of a software system including one or more resources, the computer process comprising:

generating one or more workload definition sequences defining the software system, each workload definition sequence including a plurality of workload request nodes, the workload definition sequence including at least two of the workload request nodes having a sequential relationship relative to different simulation intervals; **(Page 97, Section 3.1. Figure 1, Events/Evaluation Directives)**

receiving the workload definition sequence into an evaluation engine; **(Page 97, Section 3.1, Figure 1)**

and evaluating the one or more workload definition sequences to simulate the performance of the software system. **(Page 97, Section 3.1, Figure 1)**

Regarding Claims 2, 19, and 31:

Papaefstathiou discloses the computer program product of claim 1 wherein each request node is defined independently of a specific hardware model instance. **(Page 97, Section 3.1, Paragraph 2, Lines 7-8. Figure 1)**

Regarding Claims 3 and 32:

Papaefstathiou discloses the computer program product of claim 1 wherein each workload request node defines a transaction associated with a resource in the software system. **(Page 98, Section 3.1, Paragraph 3, Line 1)**

Regarding Claims 4, 20, and 33:

Papaefstathiou discloses the computer program product of claim 1 wherein each workload request node represents one or more component events associated with a resource in the software system, **(Page 98, Section 3.2, Paragraph 1, Lines 2-3)**

Regarding Claims 5 and 34:

Papaefstathiou discloses the computer program product of claim 1 wherein the one or more workload sequences are generated prior to the receiving and evaluating operations and substantially define all workload request nodes for simulating performance of the software system. **(Page 97-98, Section 3.1, Figure 1)**

Regarding Claims 6 and 21:

Papaefstathiou discloses the computer program product of claim 1 wherein each workload request node defines a device option characterizing constraints on how the workload request node may be assigned to a resource in the software system. **(Page 98, Section 3.1, Paragraph 3, Lines 1-3)**

Regarding Claims 7 and 22:

Papaefstathiou discloses the computer program product of claim 1 wherein at least one workload sequence includes a fork node defining a split of one workload sequence branch into a plurality of workload sequence branches. **(Page 98, Section 3.1, Paragraph 4, Line 1-2. Page 98, Section 3.2, Paragraph 1, Line 1-3)**

Regarding Claim 8 and 23:

Papaefstathiou discloses the computer program product of claim 1 wherein at least one workload sequence includes a join node defining a combination of a plurality of workload sequence branches into a single workload sequence branch. **(Page 101, Section 4, Paragraph 3, Lines 12-19)**

Regarding Claim 9:

Papaefstathiou discloses the computer program product of claim 1 wherein the computer process further comprises: receiving at least one of a monitoring trace, statistical data, and a workload specification to generate the one or more workload definition sequences. **(Page 97, Section 3.1, Paragraph 2, Lines 1-2)**

Regarding Claim 10:

Papaefstathiou discloses the computer program product of claim 1 wherein the operation of receiving at least one of a monitoring trace, statistical data, and a workload specification comprises: receiving the monitoring trace defining a sequence of software system requests relating to an application request associated with the application. **(Page 97, Section 3.1, Paragraph 2, Lines 1-2)**

Regarding Claim 11:

Papaefstathiou discloses the computer program product of claim 1 wherein the operation of receiving at least one of a monitoring trace, statistical data, and a workload specification comprises: receiving the statistical data defining a statistical distribution of one or more application requests associated with the application. **(Page 98, Section 3.1, Paragraph 3, Lines 7-11)**

Regarding Claim 12:

Papaefstathiou discloses the computer program product of claim 1 wherein the operation of receiving at least one of a monitoring trace, statistical data, and a workload

specification comprises: receiving the workload specification defining a set of resource request descriptions associated with the software system. **(Page 98, Section 3.1, Paragraph 3, Lines 1-2)**

Regarding Claims 13, 24, and 35:

Papaefstathiou discloses the computer program product of claim 1 wherein each workload definition sequence comprises a start node associated with a start time, and the simulating operation comprises: activating at least one of the workload definition sequences, if the start time associated with the start node of the workload definition sequence satisfies the simulation interval value. **(Page 102, Section 5, Figures 6 and 7)**

Regarding Claims 14, 25, 26, and 36:

Papaefstathiou discloses the computer program product of claim 1 wherein the simulation operation comprises: translating at least one of the workload request nodes into one or more component events recorded in an event queue. **(Page 102, Section 5, Figures 6 and 7)**

Regarding Claims 15, 27 and 37:

Papaefstathiou discloses the computer program product of claim 14 wherein the evaluating operation comprises: scheduling each component event with an instance of a hardware model associated with a resource in the software system. **(Page 102, Section 5, Figures 6 and 7)**

Regarding Claims 16, 28, and 38:

Papaefstathiou discloses the computer program product of claim 14 wherein the evaluating operation comprises: scheduling, based on a scheduling policy, each component event with an instance of a hardware model associated with a resource in the software system. **(Page 102, Section 5, Paragraph 4, Lines 1-3, Figures 6 and 7)**

Regarding Claims 17 and 39:

Papaefstathiou discloses the computer program product of claim 14 where the evaluating operation further comprises: receiving one of the component events from the event queue; identifying a resource associated with the component event; scheduling the component event with an instance of a hardware model associated with the resource in the software system; and simulating the component event using the instance of the hardware model. **(Page 98, Section 3.2, Paragraph 1, Lines 1-3)**

Regarding Claim 29:

Papaefstathiou discloses the performance simulation system of claim 18 wherein the evaluation engine comprises a simulator determining a duration of a component event assigning to an instance of a hardware model. **(Page 102, Section 5, Figures 6 and 7)**

Conclusion

4. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. These references include:

A) "Load Balancing Port Switching Hub", Zornig et al., U.S. Patent No. 5,742,587.

5. All Claims are rejected.

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Saif A. Alhija whose telephone number is (571) 272-8635. The examiner can normally be reached on M-F, 11:00-7:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jean Homere can be reached on (571) 272-3780. The fax phone number for the organization where this application or proceeding is assigned is (571) 273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

SAA

September 20, 2005

Application/Control Number: 10/053,733
Art Unit: 2128

Page 9


JEAN P. HOMERE
PRIMARY EXAMINER